

REMARKS

In view of the above amendments and the following remarks, reconsideration and further examination are requested.

In reply to the drawing objection as expressed in section 2 of the Office Action, the substitute specification has been amended at page 13, line 6, so as to include reference numeral -- 601--.

Additionally, a replacement formal drawing has been provided for Fig. 25, in order to correct errors noted in this figure. Namely, in the third box from the bottom, --fourth-- has been substituted for "fifth" and --third-- has been substituted for "fourth". Additionally, in the second from the last box, --fourth-- has been substituted for "fifth", and in the final box --fifth-- has been substituted for "sixth".

Claims 1-15 were rejected under 35 U.S.C. § 102(b) as being anticipated by Ehman et al. In reply to this rejection, claim 1 has been amended so as to be distinguished from Ehman et al. Specifically, claim 1 now recites

A circuit board, comprising:
 an electronic part having an electrode terminal on one surface thereof;
 a base layer;
 a first conductive circuit, manufactured by hardening a conductive paste material formed in a predetermined shape on said base layer;
 a first insulating layer, manufactured by hardening an insulating paste material formed on said base layer and said first conductive circuit, **said first insulating layer having an opening therein;**
 a second insulating layer on said first insulating layer; and
 a second conductive circuit, manufactured by hardening a conductive paste material formed in a predetermined shape on said second insulating layer,
 wherein said electronic part is within said opening such that said electronic part, but not said electrode terminal, is covered by said second insulating layer, and said electrode terminal is electrically connected to said second conductive circuit.

Thus, amended claim 1 requires a circuit board that includes an electronic part within a first

insulating layer, which electronic part is electrically connected to a conductive circuit, disposed on a second insulating layer, via a terminal of the electronic part. Please see Figures 1-9, for example, wherein "11" corresponds to the base layer, "13" corresponds to the first conductive circuit, "15" corresponds to the first insulating layer, "19" corresponds to the second conductive circuit, "4" corresponds to the electronic component, and "41" corresponds to the electrode terminal.

Such a circuit board as now recited in claim 1 is not taught or suggested by Ehman et al. In this regard, Ehman et al. discloses a method of forming a resistor and a capacitor by screen printing, but does not disclose an electronic part positioned within an insulating layer.

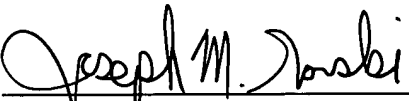
Thus, claim 1 is not anticipated by Ehman et al., whereby claims 1-15 are allowable.

In view of the above amendments and remarks, it is respectfully submitted that the present application is in condition for allowance and an early Notice of Allowance is earnestly solicited.

If after reviewing this Amendment, the Examiner believes that any issues remain which must be resolved before the application can be passed to issue, the Examiner is invited to contact the Applicants' undersigned representative by telephone to resolve such issues.

Respectfully submitted,

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